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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/074,405	02/12/2002	Naoaki Komiya	YKI-0083	7025

23413 7590 10/24/2003

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BLOOMFIELD, CT 06002

EXAMINER
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LEE, WILSON

ART UNIT	PAPER NUMBER
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2821

DATE MAILED: 10/24/2003

Please find below and/or attached an Office communication concerning this application or proceeding.

**Office Action Summary**

Application No.

10/074,405

Applicant(s)

KOMIYA, NAOAKI

Examiner

Wilson Lee

Art Unit

2821

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

- 1) ☒ Responsive to communication(s) filed on 18 July 2003.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

- 4) ☒ Claim(s) 1-12 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1,3,5 and 10-12 is/are rejected.
- 7) ☒ Claim(s) 2,4 and 6-9 is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

**Application Papers**

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 18 July 2003 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
- Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- 11) ☐ The proposed drawing correction filed on \_\_\_\_\_ is: a) ☐ approved b) ☐ disapproved by the Examiner.
- If approved, corrected drawings are required in reply to this Office action.
- 12) ☐ The oath or declaration is objected to by the Examiner.

**Priority under 35 U.S.C. §§ 119 and 120**

- 13) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some \* c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- \* See the attached detailed Office action for a list of the certified copies not received.
- 14) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).
- a) ☐ The translation of the foreign language provisional application has been received.
- 15) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

**Attachment(s)**

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449) Paper No(s) \_\_\_\_\_
- 4) ☐ Interview Summary (PTO-413) Paper No(s). \_\_\_\_\_
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: \_\_\_\_\_

### Remarks

Applicant's arguments with respect to claims 1-12 have been considered but are moot in view of the new ground(s) of rejection due to an updated search.

### Claim Rejections – 35 U.S.C. 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

Claims 1, 5, 10, 11, 12 are rejected under 35 U.S.C. 102(e) as being anticipated by Shannon et al. (6,542,138).

Regarding Claim 1, Shannon discloses an organic EL pixel circuit (See Figure 5) for controlling application of a drive voltage to a plurality of organic EL pixels, wherein a discharge transistor (40) (See Col. 13, lines 26-27) is provided for discharging charges accumulated in a capacitor (36) of an organic EL element (20), and

- a driving transistor (22) (See Col. 13, line 39) is provided between the organic EL element and a power source (32) for controlling driving current to the organic EL element (20), and one end of the discharge transistor (40) is connected between the driving transistor (22) and the organic EL element (20).

Regarding Claim 5, Shannon discloses an organic EL pixel circuit (See Figures 1 and 5) for controlling application of a drive voltage to a plurality of organic EL pixels wherein a discharge transistor (40) is provided for discharging charges accumulated in a capacitor (36) of an organic EL element (20); wherein:

- each of said pixels includes a storage capacitor (20) for holding a control voltage to be applied to a drive transistor (22) which controls application of a drive current to the organic EL element (20), and
- each of said pixels further includes a control transistor (26) for controlling the control voltage held in the storage capacitor (36) to turn the drive transistor (22) off.

Regarding Claim 10, Shannon discloses an organic EL pixel comprising:

- an EL element (20);
- a driving transistor (22) for controlling driving current to be supplied to the EL element;
- a selecting transistor (26) for controlling the driving transistor (22); and
- a discharge transistor (40) provided independently of the driving transistor (22) and the selecting transistor (26) for discharging charges accumulated in a parasitic capacitor (36) of the EL element.

Regarding Claim 11, Shannon discloses that the discharge transistor is connected to a node (41) connecting the driving transistor (22) and the EL element (20).

Regarding Claim 12, Shannon discloses that the discharge transistor is turned on when the selecting transistor is turned off since when the selecting transistor is off, the

voltage from the charge capacitor (36) turn on the drive transistor (22) and then the discharge transistor will be turned on by the EL element (20).

Claim 3 is rejected under 35 U.S.C. 102(e) as being anticipated by ting (6,486,606).

. Regarding Claim 3, Ting discloses an organic EL pixel circuit for controlling application of a drive voltage to a plurality of organic EL pixels, wherein a discharge transistor is provided for discharging, charges accumulated in a capacitor of an organic EL element wherein:

- the organic EL pixels are arranged in a matrix and the pixels in a row direction are selected by a same gate line (Scan line), and said discharge transistor (T2) is driven by a dedicated discharge line (discharge line) which is activated at a timing prior to the selection of the gate line (See Figure 3) at the row of the EL element to which said discharge transistor is connected, to discharge the charges accumulated in the capacitor (C) of the organic EL element (D).

#### **Allowable subject matter**

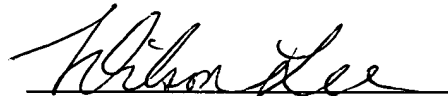
Claims 2, 4, 6, 7, 8 and 9 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

#### **Correspondence**

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Examiner Wilson Lee whose telephone number is (703) 306-3426.

Any inquiry of a general nature or relating to the status of this application should be directed to the Technology Center receptionist whose telephone number is (703) 308-0956.

Papers related to Technology Center 2800 applications may be submitted to Technology Center 2800 by facsimile transmission. Any transmission not to be considered an official response must be clearly marked "DRAFT". The Technology Center Fax Center number is (703) 308-7722 or (703) 308-7724.

A handwritten signature in black ink, appearing to read "Wilson Lee", written over a horizontal line.

Wilson Lee  
Patent Examiner  
U.S. Patent & Trademark Office

WL  
10/20/03